**VLSI DESIGN FOR CONVOLUTIVE BLIND SOURCE SEPARATION**

**Abstract:**

This will presents an efficient Very Large Scale Integration (VLSI) design for Convolutive Blind Source Separation (CBSS). Information maximization (Infomax) approach is adopted for CBSS network. CBSS chip design mainly includes Infomax filtering modules and scaling factor computation modules. In an Infomax filtering module, filtering of input samples are done by Infomax filter with the weights updated by Infomax driven stochastic learning rules. And for scaling factor computation module all operations are implemented by the circuit design based on a piecewise-linear approximation scheme. An efficient and high performance and less delayed blind source separation technique is described.

**TOOLS:**

1. **XilinxISE 14.7**

**LANGUAGE:**

1. **VerilogHDL**